

CURRICULUM VITAE

OBJECTIVE:

Looking forward to serving in the Education field with likeminded people so that I can use my skills acquired in the college thereby contributing to the growth of the organization.

EDUCATIONAL QUALIFICATION:

DEGREE	INSTITUTE	UNIVERSITY/ BOARD	YEAR	PERCENTAGE
M.Tech. (Communication Systems)	Gyan Ganga institute of Technology	Rajiv Gandhi Proudyogiki Vishwavidalaya (RGPV)	2016	89 %
B.E. (Electronics & Communication)	Kavikulguru Institute of Tech & Science	Nagpur University	2006	74 %
HSSC	Guru Nanak Jr. College, Nagpur	Maharashtra Board	2002	85 %
SSC	Guru Nanak High School, Nagpur	Maharashtra Board	2000	78 %

WORK EXPERIENCE:

ORGANIZATION	DURATION	DESIGNATION
Rishi M.S. Institute of Engineering & Technology – Hyderabad	Jul 2011 to Sep 2011	Asst. Professor
Kavikulguru Institute of Technology & Science (KITS) – Ramtek (Nagpur)	Sep 2006 to Oct 2010	Lecturer

Organization	Rishi M.S. Institute of Engineering & Technology – Hyderabad (Affiliated to JNTU - Hyderabad)
Designation	Worked as an Asst. Professor under AICTE norms in Electronics & Communication department.
Subjects undertaken	Maths (First Year B.Tech.) , Linear & Discrete Integrated Circuits.
Duration	Jul 2011 to Sep 2011

Organization	Kavikulguru Institute of Technology and Science (KITS) , Ramtek, Dist: Nagpur (Maharashtra)
Designation	Worked as a Lecturer under AICTE norms in Electronics department and worked as a staff in charge of Electromatix forum which organizes technical and non technical events.
Subjects undertaken	Signals & Systems, Mobile Communication, Communication Systems, Information theory and Data Communication, Digital & Analog Circuits.
Duration	Sept 2006 to Oct 2010(4 years)

PAPERS PUBLISHED:

- IJRSET: Review on PAPR Reduction in OFDM based system Publication: Vol 4., Issue 12 , Dec 2015.
- IJSR: Performance Improvement in OFDM using combination of Signal Scrambling and DCT Publication Vol 5., Issue 12, Dec 2016

WORKSHOPS AND SEMINARS:

- Undergone Training on MATLAB and its Simulink Applications for three days in KITS Ramtek, from 27th – 29th Dec-06.
- Attended off-campus STTP On **Optical and Wireless Networks** conducted by G.S Sanyal School of Telecommunications, IIT Kharagpur during 17th -22nd Dec-07.
- Attended STTP on **Advances in Coding and Communication Algorithms & Systems** from 26th -31st Mar-09 at RKNEC, Nagpur.
- Attended one day workshop on **PRIMEWIN- an Image processing software for remote sensing applications** conducted by ISRO, Nagpur on 1st Oct-08.
- Attended two days workshop on Orcad **Simulation a P-Spice software** conducted by J.D. College of Engineering, Nagpur from 15th -16th April-10.
- Attended one day workshop on Curriculum Development for Electronics/Electronics & Telecommunication Engineering held on 5th Dec-09, organized by S.B.Jain Institute of Technology, Management and research, Nagpur.
- Attended two days National Conference on **Recent trends in Computing and Information Processing** Conducted by KITS, Ramtek from 17th -18th February -10.

NATIONAL CONFERENCE:

- Presented paper titled: **IEEE 802.15.4 Low rate wireless personal Area Network Co-existence Issues** at National Conference UTSAV Conducted by KITS Ramtek, from 28th -29th December-09.

TECHNICAL PROJECTS:

- M. Tech. (Project) : PAPR Reduction in OFDM Systems using PTS+DCT+SLM Techniques
This project embarks mainly on the reduction of the PAPR value in an OFDM system & evaluating BER using partially transmitting data in sequencing order (PTS) in combination with the new algorithms. There are few other methods like amplitude clipping, filtering schemes which have low-complexity, but on the other side they are incurred from various issues like distortion related to frequency in-band & distortion related to out-of-band. Advanced techniques like DCT, PTS & SLM have been considered for PAPR reduction. The combination of these techniques has tried to minimize the range of PAPR effectively.
- Title: Mini project on PCB Board in 3rd year on Automatic Night light with alarm & was successfully implemented.
- Title: Major project on Speech compression and decompression using wavelets transformation in July-06. In this project we demonstrated as how the speech can be compressed at the input side using wavelet transformation technique and the mother wavelet used was Daubechies. At the receiver side speech was decompressed in order to recover the original speech sample. The main advantage of the project was bandwidth reduction which is a prime need today. Software simulation included Verilog Language which is compatible with 'C' and the hardware requirements include FPGA toolkit.

ACADEMICS ACHIEVEMENTS:

- Gold medalist in 12th for securing highest marks at school level.
- Fourth University Ranker in fifth semester.
- Seventh University Ranker in Seventh Semester.
- Awarded grade 'A' in written examination in NCC.

Profile

Sweety Narula

sweetyanarula@gmail.com

Contact: 90215 40796

PERSONAL INFORMATION:

Name	Mrs. Sweety Narula
DOB	24 TH February -1985
Marital Status	Married.
Languages Known	English, Hindi and Marathi
Postal Address	E – 405, COSMOS, Magarpatta City, Hadapsar Pune – 411013 (Maharashtra)

DECLARATION:

I Sweety Narula declare that the above-mentioned information is true to best of my knowledge and belief.

Regards,
Sweety Narula